

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF THE CLAIMS:

1. (currently amended) A memory card comprising:  
a plurality of external terminals;  
an interface unit; and  
an erasable and writable nonvolatile memory,  
wherein said plurality of external terminals include a select terminal coupled to ~~a~~ at least a pair of pull-up resistors including a low resistance value resistor and a high resistance value resistor to provide a pull-up resistance, and  
wherein said interface unit ~~selects a relatively~~ switches from said high resistance value resistor to said low resistance value resistor ~~for the pull-up resistor of said select terminal~~ before performing a mode determination for the memory card based on an input from said select terminal during a mode determination period to perform said mode determination using said low resistance value resistor, and ~~selects a relatively~~ switches back to said high resistance value resistor after said mode determination period, and then is capable of starting data input for data storing or data output for data reading from/to a device outside the memory card based on a protocol resulting from said mode determination.

2. (original) A memory card according to claim 1, wherein said mode determination for the memory card is performed in response to reception of an initialization command to the memory card.

3. (currently amended) A memory card according to claim 1, wherein, when said mode determination is performed, said interface unit sets an interface mode ~~with an outside~~ to a first operating mode in response to a HIGH level of said select terminal, and sets the interface mode ~~with the outside~~ to a second operating mode in response to a LOW level of said select terminal.

4. (currently amended) A memory card according to claim 3, wherein said memory card is based on a ~~standard of~~ Multimedia Card standard, said first operating mode is an SPI mode, and said second operating mode is an MMC mode.

5. (currently amended) A memory card according to claim 1, wherein ~~said a~~ selection of ~~the a~~ low resistance value of the pull-up resistance is performed stepwise and the stepwise selection assumes that a lower resistance value is selected earlier.

6. (currently amended) A memory card according to claim 1,

wherein said plurality of external terminals includes a  
data terminal coupling to a pull-up resistor, and

wherein said interface unit selects a relatively low  
resistance value for the pull-up ~~resistor~~ resistance of said  
data terminal during a period after a write command until a  
start bit of data to be written which is supplied to said data  
terminal is detected, and selects a relatively high initial  
resistance value after said detection of the start bit.

7. (currently amended) A memory card comprising:

a plurality of external terminals including a select  
terminal; and

an internal circuit having erasable and writable  
nonvolatile storing means,

wherein said internal circuit lowers, in a mode  
determination period for performing a mode determination in  
response to an initialization command and based on an input  
from ~~a said select terminal as one of said external terminals,~~  
a ~~resistance~~ value of a pull-up ~~resistor~~ resistance of said  
select terminal before a determination timing, and  
~~restores~~ raises the resistance value of the pull-up ~~resistor~~  
resistance to an initial resistance value after said mode  
determination period, and then starts communicating between an  
outside device of the memory card based on a protocol  
resulting from said mode determination made using said lowered  
resistance value.

8. (currently amended) A memory card comprising:  
a plurality of external terminals including a data terminal; and  
an internal circuit having erasable and writable nonvolatile storing means,  
wherein said internal circuit lowers, during a period after receiving a write command until receiving ~~[[of ]]~~ a start bit of data to be written which is supplied to ~~a~~ said data terminal as one of said external terminals is detected, a ~~resistance~~ value of a pull-up resistor ~~resistance~~ of said data data select terminal for a mode determination, and ~~restores~~ raises the resistance value of the pull-up resistor ~~resistance~~ to an initial value after said detection of the start bit, and then starts communicating between an outside device of the memory card based on a protocol resulting from said mode determination made using said lowered resistance value.

9. (currently amended) An electronic device comprising:  
a plurality of external terminals including a select terminal; and  
an internal circuit,  
wherein said internal circuit ~~relatively~~ lowers, in a mode determination period for performing a mode determination in response to an initialization command and based on an input

from a ~~said~~ select terminal ~~as one of said external terminals~~,  
an input impedance of said select terminal before a  
determination timing, ~~and returns~~ raises the input impedance to  
an initial value after said mode determination period, and  
then starts communication between an outside device of the  
memory card based on a protocol resulting from said mode  
determination made using said lowered impedance value.

10. (currently amended) An electronic device comprising:  
a plurality of external terminals including a data  
terminal; and

an internal circuit,  
wherein said internal circuit ~~relatively lowers~~, during a  
period after receiving a write command until receiving ~~of a~~  
start bit of data to be written which is supplied to a ~~said~~  
data terminal ~~as one of said external terminals~~ is detected,  
an input impedance of ~~said data~~ a select terminal for a mode  
determination, and returns raises the input impedance to an  
initial value after said detection of the start bit, and then  
starts communication between an outside device of the memory  
card based on a protocol resulting from said mode  
determination made using said lowered impedance value.